


**REVISED SCHEDULE OF (REGULAR / BACK PAPER) ODD END SEMESTER EXAMINATION DECEMBER,2025**

**B.Tech. III<sup>rd</sup> Semester**

**TIME :- 01:30PM TO 04:30PM.**

<b>DATE/DAY</b>	<b>Electronics &amp; CommunicationEngineering (ESR &amp; IOT)</b>
<b>Dec 02,2025 TUESDAY</b>	<b>TEC343 Electronics Engineering Materials</b>
<b>Dec 04,2025 THURSDAY</b>	<b>TEC303 Networks Analysis and Synthesis</b>
<b>Dec 06,2025 SATURDAY</b>	<b>TMA310 Advanced Engineering Mathematics</b>
<b>Dec 09,2025 TUESDAY</b>	<b>TEC304 Signals and Systems</b>
<b>Dec 11,2025 THURSDAY</b>	<b>TEC301 Electronic Devices and Circuits</b>
<b>Dec 13,2025 SATURDAY</b>	<b>XCS301 (ONLINE) Career Skills</b>
<b>Dec 16,2025 TUESDAY</b>	<b>TEC302 Digital Electronics</b>
<b>Dec 18,2025 THURSDAY</b>	<b>TEC359 Fundamentals of Computer Organization</b>

  
Controller of Examination  
Graphic Era (Deemed to be University  
Dehradun (Uttarakhand)